

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	@ad<="20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 375/141.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:41
L2	0	@ad<="20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 375/147.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:46
L3	0	@ad<="20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 375/267.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:46
L4	2	@ad<="20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 375/329.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:47
L5	33	@ad<="20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 375/376.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:50
L6	0	@ad<="20000609" and (frequency adj synthesizer) and (multipli\$7 adj factor) and (phase adj shifter) and ((quadrature adj phase) or BPSK) and 375/376.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:49
L7	0	@ad<="20000609" and (frequency adj synthesizer) and (multipli\$7 adj factor) and (phase adj shifter) and ((quadrature adj phase) or BPSK) and 327/47.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:49
L8	0	@ad<="20000609" and (frequency adj synthesizer) and (multipli\$7 adj factor) and (phase adj shifter) and ((quadrature adj phase) or BPSK) and 332/103.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:50
L9	0	@ad<="20000609" and (frequency adj synthesizer) and (multipli\$7 adj factor) and (phase adj shifter) and ((quadrature adj phase) or BPSK) and 329/315.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:50
L10	8	@ad<="20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 455/86.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:50

L11	8	@ad<"20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and 455/86. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:51
L12	8	@ad<"20000609" and (receiver same (frequency adj (multiplier or divider)) same ((frequency adj synthesizer) or (PLL or (phase adj lock adj loop)))) and (intermediate adj frequency) and 455/86. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/10 15:51